

Shradha Shilwant

ASSISTANT PROFESSOR

Educational Qualification: M.Tech (VLSI Design & Embedded Systems)

Experience: 2.5

Date of Joining: 10/09/2024

DOB:15/07/1990

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Areas of Interest: VLSI core, Software Testing

Memberships: -

Courses Handled: Linear Integrated Circuits, Satellite Communication, Power Electronics, Electronic measurement and Instrumentation ,C programming, Machine Learning

Administrative responsibilities:



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| Academic Coordinator |
| NSS |
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| Sr No | No of Paper Publications | Journal | | Conference |
|-------|---|-------------------------|--------------------|------------|
| | | National | 1 | |
| 1 | | International | 1 | |
| 2 | Book Publication: | Patents: | Copyrights: | |
| 3 | Any Proposal (R & D/ any others please Specify Category & No) : DPU funded Project | Projects Guided: | | |
| 4 | FDP / Workshops Attended | FDP | Workshops | |
| | | NA | NA | |
| 5 | FDP / Workshops Organized | FDP | Workshops | |
| | | NA | NA | |
| 6 | Industrial Visits Attended | NA | | |
| 7 | Certifications | NPTEL | Others | |
| | | NA | NA | |

Interaction with Outside world: IT industry (Software Developer/Tester)

Awards/Accolades/Certifications/Achievements: Internship at Research and Development at National Aerospace Laboratory, Bangalore